



09-26-02

AG/2815

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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/479,708	
	Filing Date	01/07/2000	
	First Named Inventor	Allen P. Mills	
	Group Art Unit	2815	
	Examiner Name	M. Warren	
Total Number of Pages in This Submission	19	Attorney Docket Number	Mills-11

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Serial No. 09/479,708

MILLS 11



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Patent Application of:
MILLS, ALLEN P.

Serial No.: **09/479,708**

Filed: **January 7, 2000**

Title: **METHOD AND APPARATUS FOR
TEMPERATURE COMPENSATION OF
READ-ONLY MEMORY**

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

RESPONSE TO FINAL OFFICE ACTION DATED July 2, 2002

In response to the Office Action of July 2, 2002, and in accordance
with Rule 116 of the Rules of Practice, reconsideration is respectfully
requested in view of the following remarks:

Version Without Markings

IN THE CLAIMS

- 1 1. (Twice Amended) In a ROM device, a temperature compensation circuit to
- 2 maintain a current through a selected one of a plurality of data
- 3 resistors substantially constant comprising:
- 4 at least one reference resistor, wherein the conductivity of said
- 5 reference resistor is responsive to changes in temperature;

Group Art Unit: **2815**

Examiner: **M. Warren**

Atty. Dkt.: **MILLS-11**

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Okay
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#2/ C w/ Drawing
(14) Changes
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Schultz